

6/1A
7/1/00
M. Friedman

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

H. MIYAGAWA et al

Serial No.

Filed: March 4, 2002

For: MODULATION SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND TESTING METHOD FOR OSCILLATION CIRCUIT

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to the examination thereof, please amend the above-identified application as follows.

IN THE CLAIMS

Please rewrite claims 4-7 as set forth below.

A) 4. (Amended) A modulation semiconductor integrated circuit device according to claim 1, wherein said circuit of producing the second control voltage includes a digital filter which samples a digital transmission data signal and implements a computation for the sampled signal, and a D/A conversion circuit which implements the D/A conversion for the